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OF COUNSEL
MARIANNE M. DOWNING

January 29, 2007

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RE:

U.S. Patent of Alberto Bilotti, et al.

Patent No.:

7,085,119

Issued on:

August 1, 2006

Entitled:

MAGNETIC POLE INSENSITIVE SWITCH CIRCUIT

U.S. Appl. No.: 10/623,974

Our Ref. No.:

ALLEG-012CUS

Dear Sirs:

Enclosed is a completed copy of Form PTO/SB/44. It is requested that a Certificate of Correction be issued to correct the listed mistakes in the above-identified patent.

Enclosed is electronic payment of the \$100.00 fee to cover the cost of the Certificate of Correction in accordance with 37 C.F.R.§1.20(a). If there are any additional fees, please charge Deposit Account 01-0854 referencing Attorney Docket: ALLEG-012CUS.

Regards,

wdith C. Crowley

jcc@dc-m.com

JCC/nrm Enclosures 44771

PTO/SB/44 (02-01)

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

7,085,119

DATED

August 1, 2006

INVENTOR(S):

Bilotti et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 54, delete "a magnetic" and replace with -- of a magnetic --.

Column 1, line 67, delete "not physical" and replace with -- not a physical --.

Column 2, line 5, delete "as a solid" and replace with -- as solid. --.

Column 2, line 17, delete "hall" and replace with -- Hall--.

Column 3, line 43, delete "maybe" and replace with -- may be --.

Columns 4-5, lines 67-1, delete "first and second and comparators" and replace with -- first and second comparators 36, 38 --.

Column 5, lines 20-21 "output voltage $-V_{OUT}$." and replace with -- output voltage V_{OUT} .--.

Column 5, line 31, delete "vice-versa" and replace with -- vice versa --.

Column 7, line 2, delete " B_{OP} the output" and replace with -- B_{OP} , the output --.

Column 7, line 28, delete "that column" and replace with -- that the column --.

MAILING ADDRESS OF SENDER:

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Column 7, lines 63-64, delete "by each the two" and replace with -- by each of the two --.

Column 8, line 4, delete "the inverter the inverter circuit 70." and replace with -- the inverter circuit 70. --.

Column 8, lines 16-17, delete "buffer circuit 68a, 68b" and replace with -- buffer circuits 68a, 68b --.

Column 8, line 62, delete "vice-versa" and replace with -- vice versa --.

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